

23448



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re United States Patent Application of: 2771-546-CIP2

Applicants: DIMEO JR., Frank, et al. Conf. No.: 2238

Date Filed: February 23, 2004 Examiner: Not Yet Assigned

Title: APPARATUS AND PROCESS Customer No.:

FOR SENSING FLUORO

SEMICONDUCTOR PROCESSING SYSTEMS

SPECIES IN

IG SYSTEMS

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Respectfully submitted,

Vincent K. Gustafson

Reg. No. 46,182

Attorney for Applicants

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Enclosures: IDS Forms [2 pg(s).] Non-Patent References [9 pgs.] Return Postcard

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form 1449A/PTO FIFTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT

10/784,750 Application Number Filing Date 2/23/2004 First Named Inventor Dimeo Jr. et al. Art Unit 2192 **Examiner Name** Ben C. Wang

(Use as many sheets as necessary)

Substitute

Attorney Docket Number 2771-546-CIP2 Sheet

			U.S. PATENT	DOCUMENTS	
Examiner	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
Initials*		Number - Kind Code ^{2 (if known)}			
	во	US- 6,443,179 - B1	09-03-2002	Benavides, Gilbert L., et al.	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²			
	CF	VAN ZANT, PETER, Chapter 8: The ten-step patterning process Surface preparation to exposure, Microchip Fabrication: A Practical Guide to Semiconductor Processing, 5th Ed., 2004, Page(s) 197-203, Publisher: McGraw-Hill, Published in: New York, NY				
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